RoHS
COMPLIANT

Moisture Æ r ‘ ‘ o p ψls

Signal amplification
Switching circuit

J-STD-002 and JESD22-B102

■ (Ta=25 unless otherwise noted)

		8 Q L W	& R Q G L W L R Q V	9 D O X H
Device marking code				619L
Collector-base voltage	V_{CBO}	V	I_C=100μA,IE=0	50
Collector-emitter voltage	V_{CEO}	V	I_C =10mA,IB=0	50
Emitter-base voltage	V_{EBO}	V	I_E=100μA,IC=0	5
Collector current	I_C	A		2
Power dissipation	P_D	mW		200
Junction temperature	T_J			-55 to +150
Storage temperature	T_{STG}			-55 to +150

■ (Ta=25 unless otherwise noted)

Collector-base breakdown voltage	V_{CBO}	V	I_C=100μA,I_E=0	50		
Collector-emitter breakdown voltage	V_{CEO}	V	I_C =10mA,I_B=0	50		
Emitter-base breakdown voltage	V_{EBO}	V	I_E=100μA,I_C=0	5		
Collector-base cut-off current	I_{CBO}	uA	V_{CB}=40 V ,I_E=0			0.1
Emitter-base cut-off current	I_{EBO}	uA	V_{EB}=4 V , I_C=0			0.1
DC current gain	h_{FE1}		V_{CE}=2V,I_C=10mA	200		
	h_{FE2}		V_{CE}=2V,I_C=200mA	300		
	h_{FE3}		V_{CE}=2V,I_C=1A	200		
	h_{FE4}		V_{CE}=2V,I_C=2A	100	Tin plated leads, solderable p	

Collector-emitter saturation voltage	$V_{CE(sat)1}$	V	I_C=100mA,I_B=10mA			0.02
	$V_{CE(sat)2}$	V	I_C=1A,I_B=10mA			0.2
	$V_{CE(sat)3}$	V	I_C=2A,I_B=100mA			0.22
Base-emitter saturation voltage	$V_{BE(sat)*}$	V	I_C=2A,I_B=50mA			1
Base-emitter voltage	V_{BE}	V	V_{CE}=2V,I_C=2A			1
Transition frequency	f_T	MHz	V_{CE}= 5V, I_C=100mA, f=100MHz	100		
Collector-base output capacitance	Cob	pF	V_{CB}=10V ,I_E=0, f=1MHz			20

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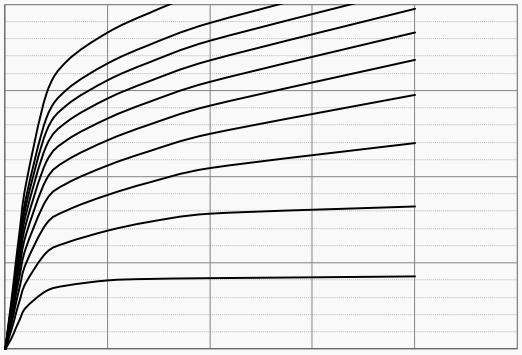
Thermal resistance, junction-to-ambient	RθJ-A(1)	/W	625
Thermal resistance, junction-to-case	RθJ-C(1)	/W	500

■ (Example)

FMMT619L	F2	Approximate 0.014	3000	30000	120000	7” reel

(Typical)



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