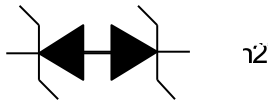


1-Line, Bi-directional, Transient Voltage Suppressor



Features

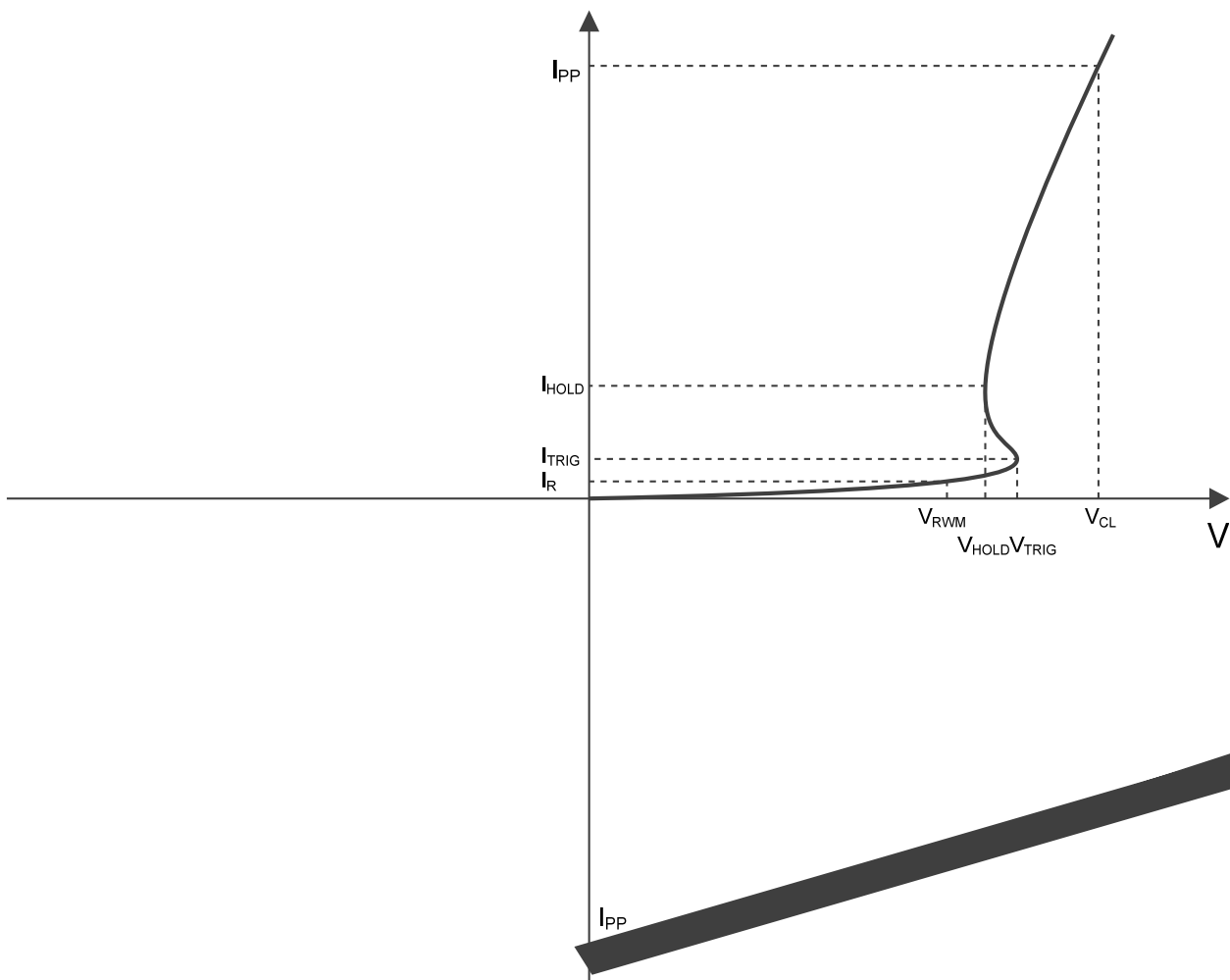
- Stand-off voltage: $\pm 5V$ Max
- Transient protection for each line according to IEC61000-4-2 (ESD): $\pm 30kV$ (contact)
IEC61000-4-4 (EFT): $80A$ (5/50ns)
IEC61000-4-5 (surge): $85A$ (8/20 μs)
- Ultra-low capacitance: $C_J = 180pF$ typ
- Low leakage current
- Low clamping voltage: $V_{CL} = 7V$ typ. @ $I_{PP} = 16A$ (TLP)
- Solid-state silicon technology

Mechanical Data

- **Package:** DFN1006-2L
- **Terminals:** Tin plated leads, solderable per J-STD-002 and JESD22-B102
- **Polarity:** No marking on bi-directional types
- **Marking:**



■ Definitions of electrical characteristics



ESD5V0LBA3

■Maximum Ratings

PARAMETER	SYMBOL
Peak pulse power (t _p = 8/20μs)	P _{pk}
Peak pulse current (t _p = 8/20μs)	I _{PP}
ESD according to IEC61000-4-2 air discharge	V _{ESD}
ESD according to IEC61000-4-2 contact discharge	
Junction temperature	T _J
Operating temperature	T _{OP}
Storage temperature	T _{STG}

Rating	UNIT
980	W
85	A
±30	KV
±30	KV
125	°C
-40~85	°C
-55~150	°C

■Electrical Characteristics T_a=25 ℃ Unless otherwise specified

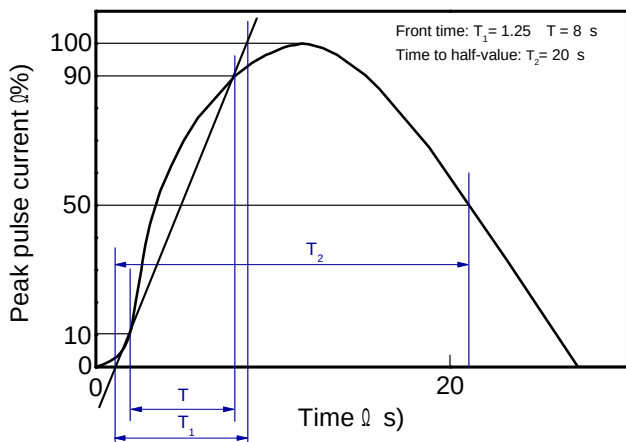
	PARAMETER	Symbol	UNIT	Co
--	-----------	--------	------	----

ns	Min	Typ	Max
			±5
			1
5.1			
	7		
	0.05		
	8		
	5.5	7.5	

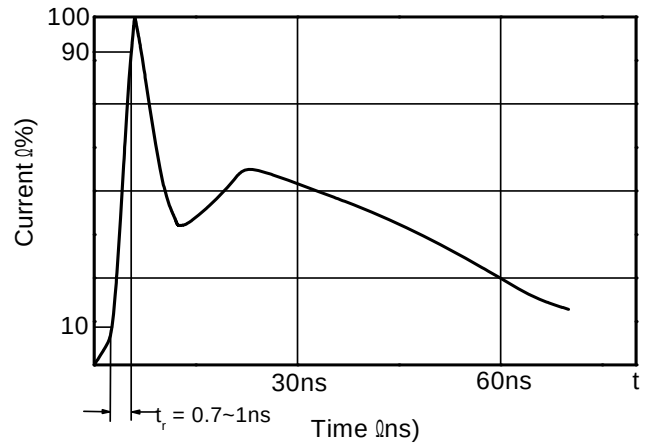


■ Characteristics (Typical)

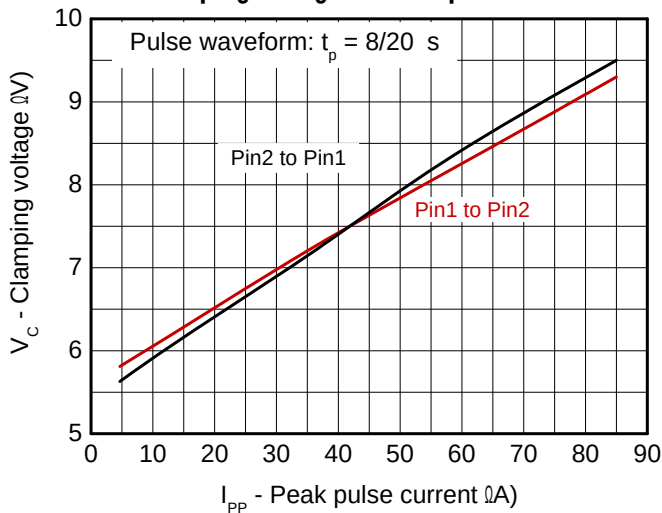
8/20 μ s waveform per IEC61000-4-5



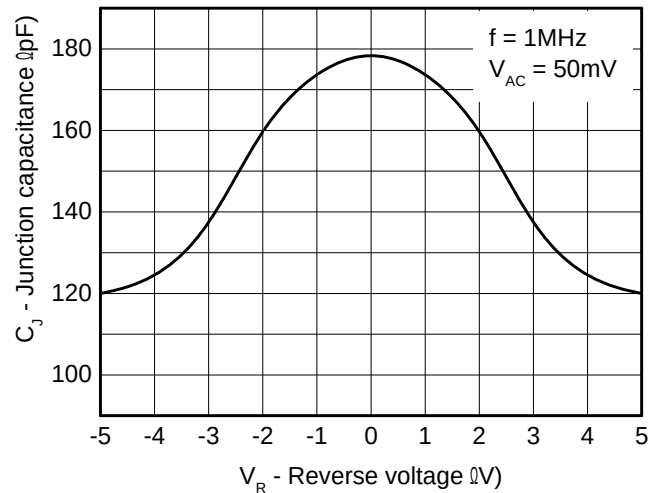
Contact discharge current waveform per IEC61000-4-2



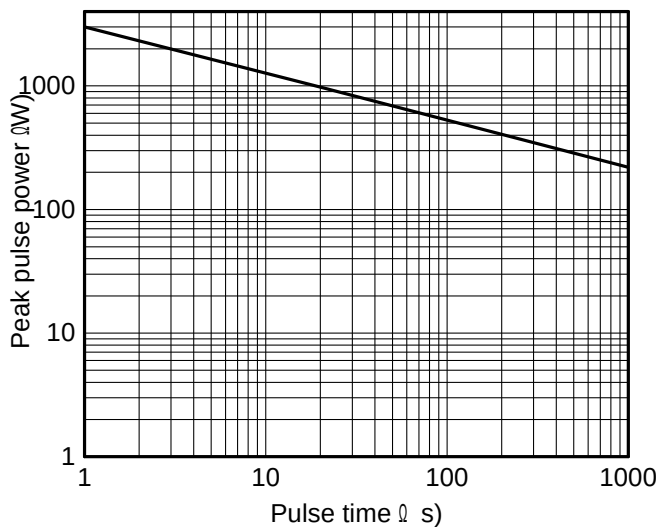
Clamping voltage vs. Peak pulse current



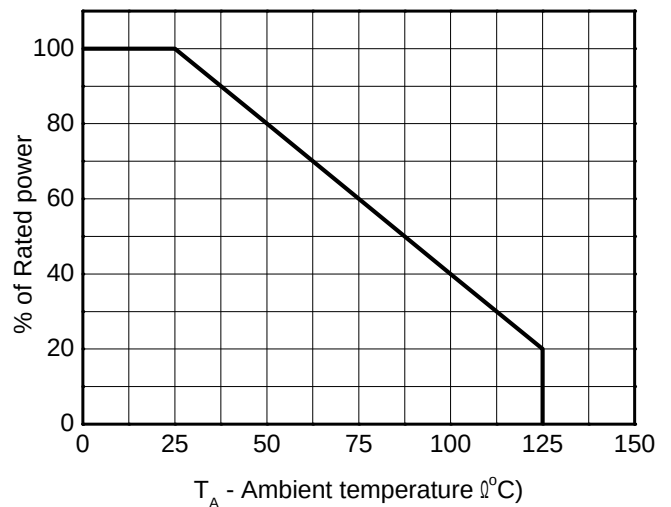
Capacitance vs. Reverse voltage



Non-repetitive peak pulse power vs. Pulse time



Power derating vs. Ambient temperature



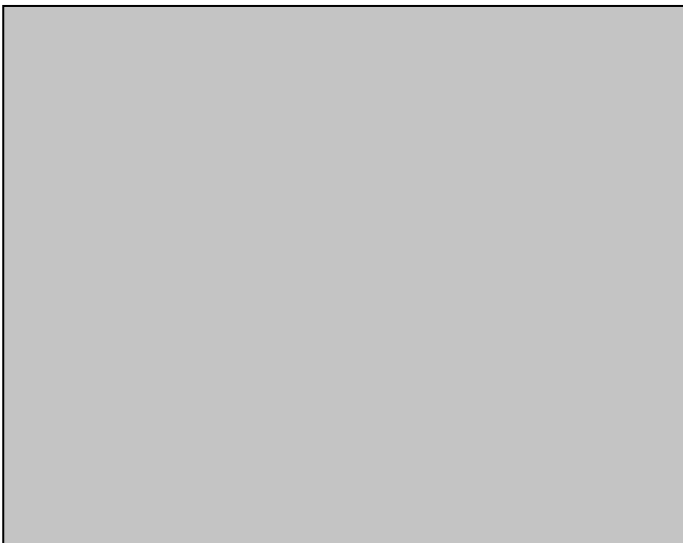


ESD5V0LBA3

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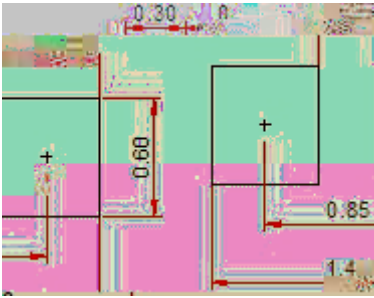
ESD clamping

ESD clamping





Recommend land pattern (Unit:mm)



Notes:

This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met



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