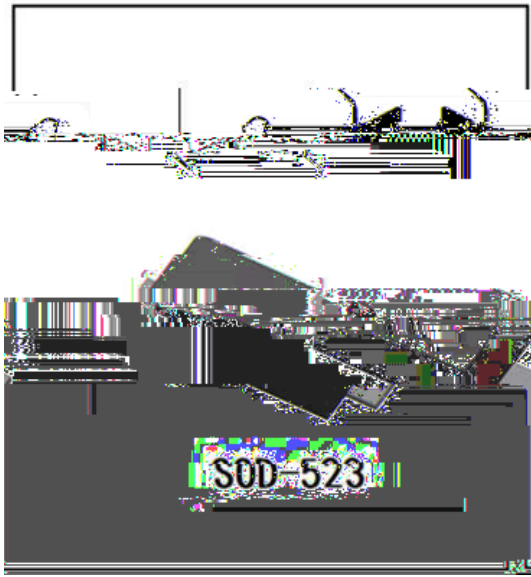


1-Line, Bi-directional, Transient Voltage Suppressor



Features

- Stand-off voltage: $\leq 5V$ Max
- Transient protection for each line according to IEC 61000-4-2 (ESD): $\leq 30kV$ (contact)
- IEC 61000-4-4 (EFT): 80A (5/50ns)
- IEC 61000-4-5(surge): 20A (8/20 μ s)
- Ultra-low capacitance: $\leq 35pF$ typ
- Low leakage current
- Low clamping voltage: $\leq 8V$ typ. @ $\pm 16A$ (TLP)
- Solid-state silicon technology

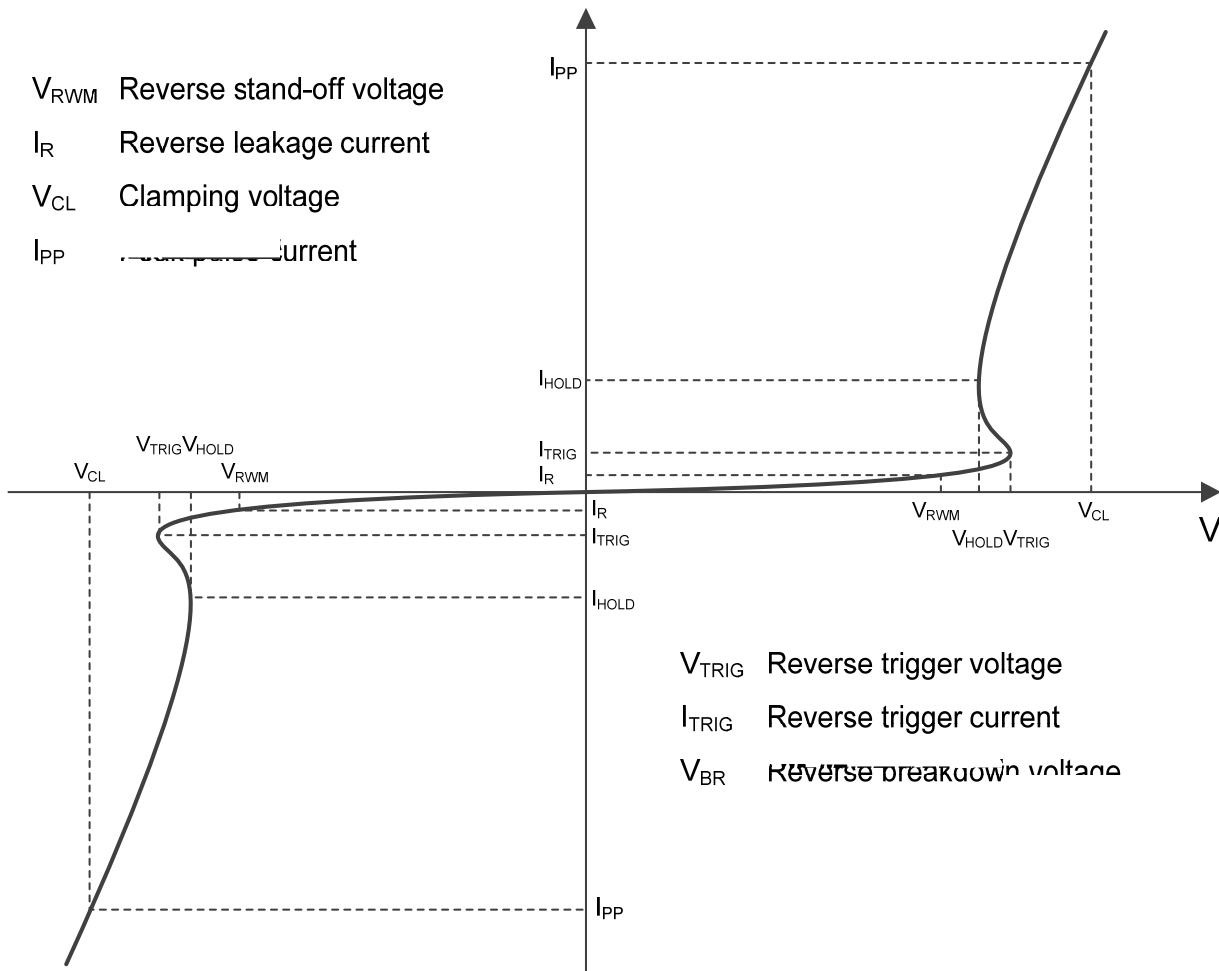
Mechanical Data

- Package: SOD-523
- Terminals: Tin plated leads, solderable per J-STD-002 and JEDEC-22-B10.2
- Polarity: No marking on bi-directional types
- Marking:



Definitions of electrical characteristics

- V_{RWM} Reverse stand-off voltage
- I_R Reverse leakage current
- V_{CL} Clamping voltage
- I_{PP} Peak pulse current





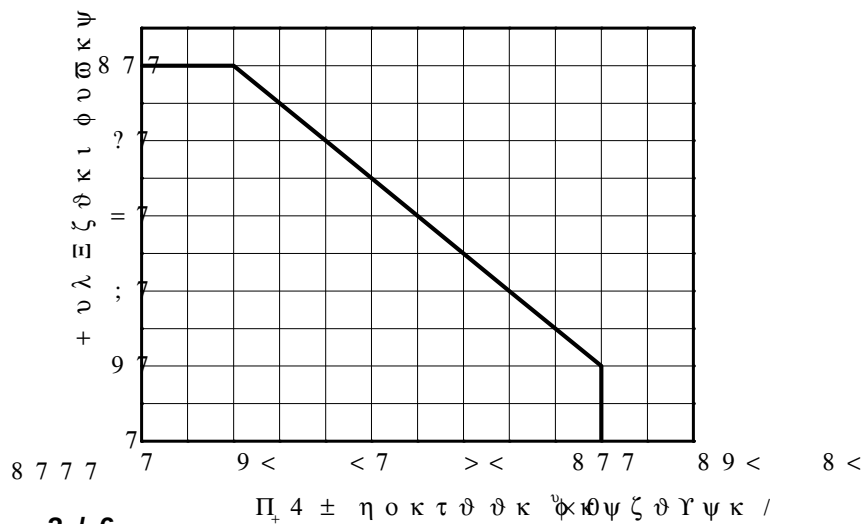
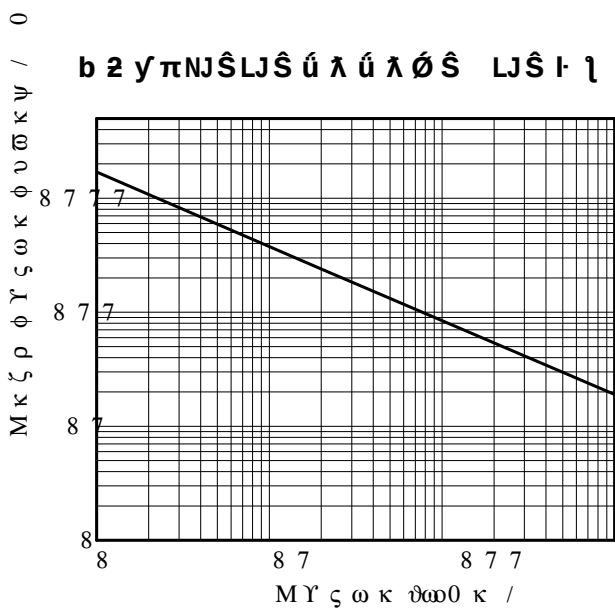
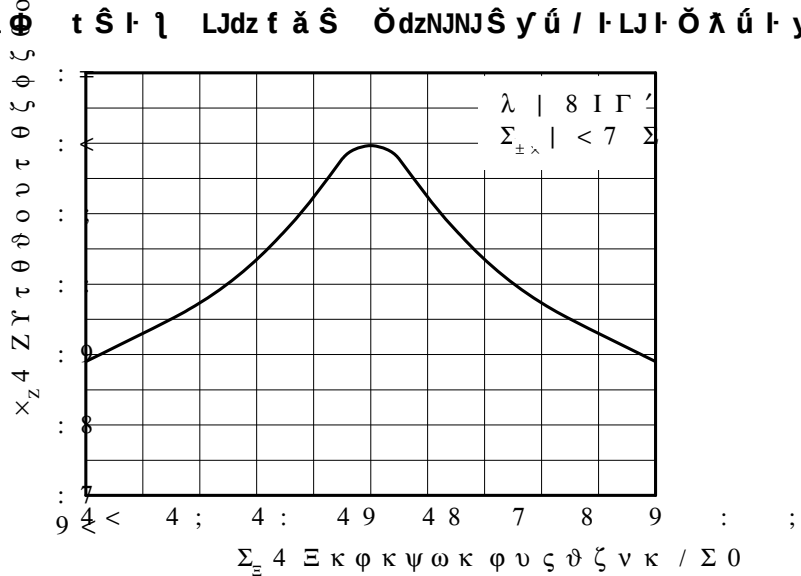
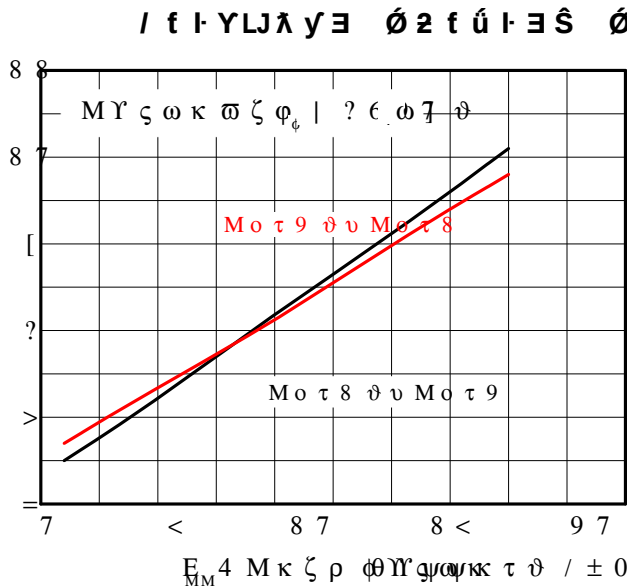
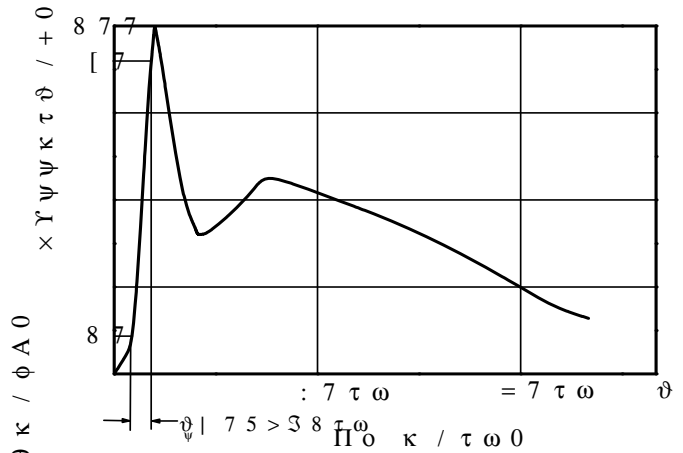
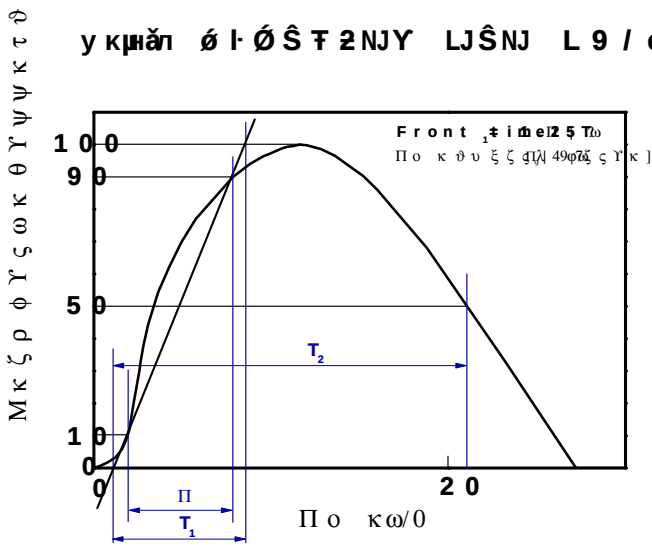
ESD5V0D5BA

Maximum Ratings

		UNIT
Peak pulse power	2.4	W
Peak power	2.0	A
ESD	0	
Junction temperature	5	
	~ 85	
	~ 150	
		~ 5.0
		100
	50mA	5.1
	5	
2)		
3)	9	



Characteristics (Typical)

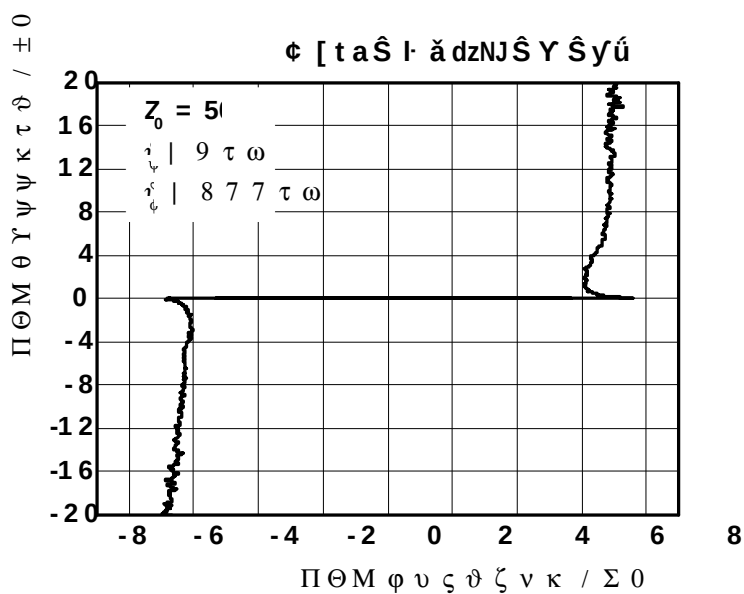
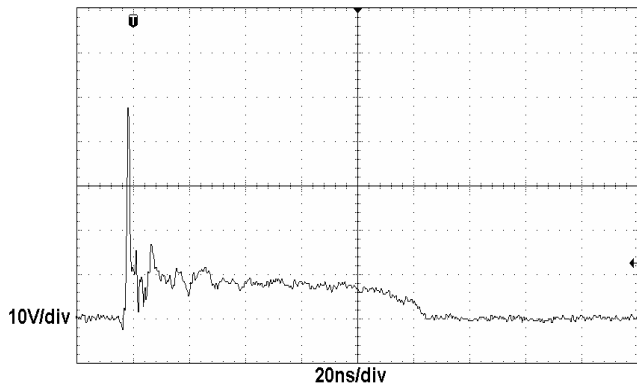




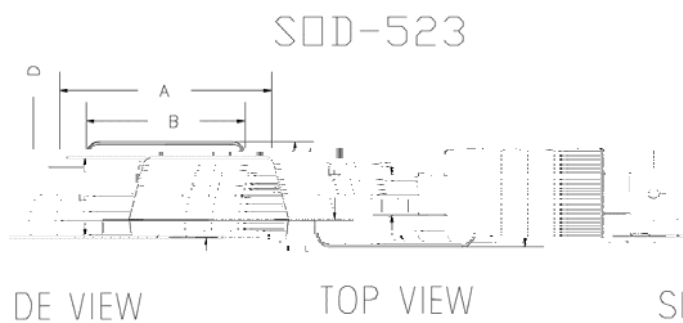
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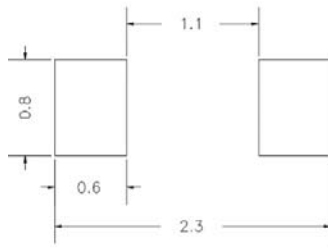
Outline Dimensions



DIMENSIONS				
DIM	INCHES		MM	
	MIN	MAX	MIN	MAX
A	0.059	0.067	1.500	1.700
B	0.043	0.051	1.100	1.300
C	0.025	0.035	0.010	0.014
D	0.050	0.060	0.002	0.008
E	0.020	0.028	0.008	0.008



Recommend land pattern (Unit:mm)



UNIT : mm

SUGGESTED SOLDER PAD LAYOUT

Notes:

This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met.



Disclaimer

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The product listed herein is designed to be used with ordinary electronic equipment or devices, and not designed to be used with special equipment.